



DOCKET NO. S. KAPUR 5-10

PATENT

#18/C
5-8-03
Hall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sharad Kapur, *et al.*

Serial No.: 09/427,238

Filed: October 26, 1999

For: SYSTEM AND METHOD FOR DETERMINING CAPACITANCE
FOR LARGE-SCALE INTEGRATED CIRCUITS

Group No.: 2123

Examiner: Eduardo Garcia-Otero

RECEIVED

MAY 06 2003

Technology Center 2100

Commissioner for Patents
Washington, D. C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on
04/28/2003 (Date)
Sharad Kapur (Printed or typed name of person signing the certificate)
Sharad Kapur (Signature of the person signing the certificate)

Sir:

PRELIMINARY AMENDMENT

Before re-examination of the above-identified application, please amend it as set forth below.